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LSI LOGIC LEGAL IP → 917037469195

NO.510 D001



Facsimile Transmittal Letter

LSI Logic Corporation Legal Department - IP 1621 Barber Lane, MS D-106 Milpitas, CA 95035

Date March 11, 2004		
Number	of pages including cover sheet	8

To: Office of Initial Patent Examination's **Filing Receipt Corrections**

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REMARKS:		Urgent		For your review	1	Reply ASAP	☐ P	ease comment
Application Filing date: First named Attorney do	: d inventor:	<u>Jeffre</u>	aber 21, 2 y S. Brow					
Tra	nsmitted he	rewith:	•					
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D002

IN THE	INTIED STATES LYI	LENI AND I KADEMAKK OFFICE	
In re Applicati	on of :	§	
	Jeff Brown et. al.	§ 8	
Serial No.:	10/719,878	§ Group Art Unit: 2858	
Filed:	November 21, 2003	<u>Examiner :</u>	

Filed: November 21, 2003 Device And Method For Using A For:

Lessened Load To Measure Signal Skew At The Output Of An

Integrated Circui

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450,

Alexandria, VA 22313-1450, on the date below:

Atty Docket:

0940

5201-27900 / 03-

REQUEST FOR CORRECTED FILING RECEIPT

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Attached is a copy of the Official Filing Receipt received from the U.S. Patent and Trademark Office in connection with the above application for which issuance of a corrected Filing Receipt is respectfully requested.

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	Applicant(s) name	Jeff Brown	
Ì	Applicant(s) address		
	Title of Application	Device And Method Fo	r Using A Lessened Load To Measure Signal Skew At The Output
	X Filing Date		11/21/03
	Serial Number	10/719,878	
	Foreign/PCT Applen. Re:		
	Other:		
	The correction(s) is/a	re not due to any e	cror by applicant and no fee is due.
	1 1	by authorized to cl	s/are due to applicant(s) error and the harge the fee of \$25.00 under 37 CFR 1.19(h) to
	Logic Corporation		Respectfully submitted,
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3/3/2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

5201-27900 Atty. Dkt. No. 03-0940 § § § Inventor(s): Jeffrey S. Brown Craig R. Chafin Title: DEVICE AND METHOD FOR USING A LESSENED LOAD TO MEASURE SIGNAL SKEW AT THE OUTPUT

OF AN INTEGRATED CIRCUIT

CERTIFICATE OF EXPRESS MAIL UNDER 37 C.F.R. § 1.10

Express Mail" mailing label no. EV403685447US Date of Deposit: November 21, 2003

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UTILITY PATENT APPLICATION TRANSMITTAL (For new non-provisional applications under 37 CFR § 1.53(b))

Ş ş §

Enclosed are the following items:

The Commissioner is hereby authorized to charge the filing fee to LSI Logic Corporation deposit 1. account no. 12-2252/03-0940. A duplicate of this page is enclosed. The filing fee is calculated as shown below.

		X =				
Total Claims	20	-20 =	0	x \$18.00 =	\$0.00	
Independent Claims	3	-3.=	0	x \$86.00 =	\$0.00	
Multiple Dependent Claims				Fee:	·	
				Basic Fee:	\$770.00	
				Total:	\$770.00	

- Request & Certification under 35 U.S.C. 122(b)(2)(B)(i). 2.
- Patent application which includes: 12 pg. specification; 4 pg. claims (1-20); 1 pg. abstract. 3.
- 4. Informal drawings of Figs. 1-5 on 3 sheets.
- Newly executed Declaration/Power of Attorney. 5.
- Newly executed Assignment w/cover sheet. 6.
- 7. Return receipt postcard.

dly submitted,

_ Daffer Reg. No. 34,146 Attorney for Applicants

Conley Rose, P.C. P.O. Box 684908 Austin, TX 78768-4908 Date: November 21, 2003

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Page 1 of 1

PATENT 5201-27900 03-0940

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Pamela Gerik

DEVICE AND METHOD FOR USING A LESSENED LOAD TO MEASURE SIGNAL SKEW AT THE OUTPUT OF AN INTEGRATED CIRCUIT

By:

Jeffrey S. Brown 3624 Goodell Lane Ft. Collins, Colorado 80528 Citizenship: USA

Craig R. Chaffu 627 Paradise Lane Colorado Springs, Colorado 80904 Citizenship: USA

Agy. Dkt. No. \$201-27900

Kevin L. Daffer Conley Rose, P.C. P.O. Box 684908 Austin, TX 78768-4908 Pb: (512) 476-1400



United States Patent and Trademark Office

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,	10/719,878	-11/24/2003-	2858	770	5201-27900 03-0940	3	20	3

11/21/03

Leo Peters
LSI Logic Corporation
1621 Barber Lane, MS D-106
Milpitas, CA 95035

16:17

CONFIRMATION NO. 3082

FEB 2 4 2004

Date Mailed: 02/20/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order—and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Jeffrey S. Brown, Ft. Collins, CO; Craig R. Chafin, Colorado Springs, CO;

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted: 02/20/2004

Projected Publication Date: Request for Non-Publication Acknowledged

Non-Publication Request: Yes

Early Publication Request: No

Title

Device and method for using a lessened load to measure signal skew at the output of an integrated circuit

Preliminary Class

324

16:17

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